

CLAIMS

What is claimed is:

- 1    1.    A picture display frame comprising:
  - 2            (a) a wireless receiver that operates to receive a digital picture from an
  - 3            external source;
  - 4            (b) a flat panel display that operates to render the digital picture;
  - 5            (c) a processor coupled to the wireless receiver and the flat panel display that
  - 6            operates to control said receiving and rendering of said digital picture; and
  - 7            (d) a frame having constructs that encircle the flat panel display, and form a
  - 8            housing to house the wireless receiver and the processor.
- 1    2.    The picture display frame as set forth in Claim 1, wherein the digital picture is
- 2            a digital painting or graphics rendition.
- 1    3.    The picture display frame as set forth in Claim 1, wherein the digital picture is
- 2            a digital photograph.
- 1    4.    The picture display frame as set forth in Claim 1, wherein the display frame
- 2            further includes a non-volatile memory coupled to the wireless receiver, the flat
- 3            panel display and the processor, that operates to store the received digital picture,
- 4            the processor further controlling said storing of the digital picture.

1     5.     The picture display frame as set forth in Claim 4, wherein the non-volatile  
2     memory is one of a block erasable flash memory, an electrically erasable  
3     programmable read only memory (EEPROM), and a complementary metal oxide  
4     semiconductor (CMOS) memory.

1     6.     The picture display frame as set forth in Claim 1, wherein the processor is  
2     one of a 8-bit or more microcontroller, a 16-bit or more digital signal processor and a  
3     32-bit or more general purpose microprocessor.

1     7.     The picture display frame as set forth in Claim 1, wherein the frame includes  
2     hanging features that facilitate hanging of the picture display frame.

1     8.     The picture display frame as set forth in Claim 1, wherein the frame includes  
2     support features that facilitate flat surface placement of the picture display frame.

1     9.     A wall mounted picture display frame comprising:

2           (a) a wireless receiver that operates to receive a digital picture from an  
3     external source;

4           (b) a flat panel display that operates to render the stored digital picture;

5           (c) a processor coupled to the wireless receiver and the flat panel display that  
6     operates to control said receiving and rendering of said digital picture; and

7           (d) a frame having constructs that encircle the flat panel display, and form a

8 housing to house the wireless receiver and the processor, the frame further having  
9 hanging features that facilitate hanging of the picture display frame.

1 10. The picture display frame as set forth in Claim 9, wherein the digital picture is  
2 a digital painting or graphics rendition.

1 11. The picture display frame as set forth in Claim 9, wherein the digital picture is  
2 a digital photograph.

1 12. The picture display frame as set forth in Claim 9, wherein the display frame  
2 further includes a non-volatile memory coupled to the wireless receiver, the flat  
3 panel display and the processor, that operates to store the received digital picture,  
4 the processor further controlling said storing of the digital picture.

1 13. The picture display frame as set forth in Claim 12, wherein the non-volatile  
2 memory is one of a block erasable flash memory, an electrically erasable  
3 programmable read only memory (EEPROM), and a complementary metal oxide  
4 semiconductor (CMOS) memory.

1 14. The picture display frame as set forth in Claim 9, wherein the processor is  
2 one of a 8-bit or more microcontroller, a 16-bit or more digital signal processor and a  
3 32-bit or more general purpose microprocessor.

1 15. A flat surface placement picture display frame comprising:  
2 (a) a wireless receiver that operates to receive a digital picture from an  
3 external source;  
4 (b) a flat panel display that operates to render the stored digital picture;  
5 (c) a processor coupled to the wireless receiver and the flat panel display,  
6 that operates to control said receiving and rendering of said digital picture; and  
7 (d) a frame having constructs that encircle the flat panel display, and form a  
8 housing to house the wireless receiver and the processor, the frame further having  
9 support features that facilitate flat surface placement of the picture display frame.

1 16. The picture display frame as set forth in Claim 15, wherein the digital picture  
2 is a digital painting or graphics rendition.

1 17. The picture display frame as set forth in Claim 15, wherein the digital picture  
2 is a digital photograph.

1 18. The picture display frame as set forth in Claim 15, wherein the display frame  
2 further includes a non-volatile memory coupled to the wireless receiver, the flat  
3 panel display and the processor, that operates to store the received digital picture,  
4 the processor further controlling said storing of the digital picture.

1 19. The picture display frame as set forth in Claim 18, wherein the non-volatile  
2 memory is one of a block erasable flash memory, an electrically erasable

- 3 programmable read only memory (EEPROM), and a complementary metal oxide
- 4 semiconductor (CMOS) memory.

- 1 20. The picture display frame as set forth in Claim 15, wherein the processor is
- 2 one of a 8-bit or more microcontroller, a 16-bit or more digital signal processor and a
- 3 32-bit or more general purpose microprocessor.